Docket No.: 0925-0220PUS1

App No.: 10/541,611 Inventor: Satoshi YAMANAKA et al.

Title: PIXEL INTERPOLATION CIRCUIT, PIXEL

INTERPOLATION METHOD AND IMAGE SCANNER

ANNOTATED SHEET

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FIG.18



	T1	Т2	Т3	Т4
M1	0	127.5	127.5	0
M2	127.5	127.5	127.5	0
М3	0	127.5	0	0

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S1	0
S2	-2

FIG.20

	T1	Т2	Т3	T4
M1	-1	1	1	-1
M2	-1	1	-1	-1

S1	255
S2	382.5
S3	127.5